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## NOTICE OF ALLOWANCE AND FEE(S) DUE

23446 7590 12/03/2009 MCANDREWS HELD & MALLOY, LTD 500 WEST MADISON STREET EXAMINER

FRANKLIN, RICHARD B

ART UNIT PAPER NUMBER

2181 DATE MAILED: 12/03/2009

500 WEST MADISON STREET SUITE 3400 CHICAGO, IL 60661

 APPELCATION NO.
 FILING DATE
 FIRST NAMED INVENTOR
 ATTORNEY DOCKET NO.
 CONFIRMATION NO.

 10/692.957
 10/24/2003
 Anand Pande
 14/920US01
 2011

TITLE OF INVENTION: SYSTEM FOR DESIGNING DATA STRUCTURES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/03/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1:313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

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II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

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10/692,957	10/24/2003		Anand Pande		I4920US01		2011
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nonprovisional	NO	\$1510	\$300	\$0		\$1810	03/03/2010
EXAM	INER	ART UNIT	CLASS-SUBCLASS				
FRANKLIN,	RICHARD B	2181	710-052000	_			
"Fee Address" ind. PTO/SB/47; Rev 03-0 Number is required.  3. ASSIGNEE NAME A	ondence address (or Cha 3/122) attached. ication (or "Fee Address 2 or more recent) attach	mge of Correspondence  "Indication form and Use of a Custome  A TO BE PRINTED C	ce or agents OR, altern (2) the name of a si registered attorney of 2 registered patent a listed, no name will  ON THE PATENT (print or	to 3 registered pate atively, agle firm (having as or agent) and the na ttorneys or agents. I be printed.	a memi nes of u f no nar	per a 2proto	ocument has been filed for
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10/692,957	10/24/2003	Anand Pande	14920US01	2011	
23446 7	590 12/03/2009		EXAMINER		
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500 WEST MADI	SON STREET	ART UNIT	PAPER NUMBER		
SUITE 3400 CHICAGO, IL 60661			2181 DATE MAILED: 12/03/200	9	

## Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 294 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 294 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

## Application No. Applicant(s) 10/692 957 PANDE, ANAND Notice of Allowability Examiner Art Unit RICHARD FRANKLIN 2181 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308. This communication is responsive to the RCE and amendment filed 23 September 2009. 2. The allowed claim(s) is/are 27-44. 3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). b) \( \subseteq \text{Some\* c} \) \( \subseteq \text{None of the:} \) 1. T Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)). \* Certified copies not received: \_\_\_\_\_. Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. CORRECTED DRAWINGS (as "replacement sheets") must be submitted. (a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d). 6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL. Attachment(s) 1. | Notice of References Cited (PTO-892) 5. Notice of Informal Patent Application 2. Notice of Draftperson's Patent Drawing Review (PTO-948) Interview Summary (PTO-413), Paper No./Mail Date Information Disclosure Statements (PTO/SB/08). 7. X Examiner's Amendment/Comment Paper No./Mail Date 4. T Examiner's Comment Regarding Requirement for Deposit 8. X Examiner's Statement of Reasons for Allowance of Biological Material 9. ☐ Other . /NIKETA I. PATEL/

Primary Examiner, Art Unit 2181

### EXAMINER'S AMENDMENT

 An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Michael Cruz (Reg. No. 44,636) on 19 November 2009.

The application has been amended as follows:

Amend the Title of the application to read:

SYSTEM FOR DESIGNING DATA STRUCTURES

### Amend claim 27 to read:

A first-in-first-out (FIFO) data system, comprising:

a FIFO memory that has a binary write pointer input, a binary read pointer input, a write data input, a write clock input, and a read data output, wherein the FIFO memory comprises a data structure of depth d from which data may be written into using the write data input and read out using the read data output, wherein d is not a value of 2<sup>n</sup> in which d and n are integers;

an output of a first set of flip flops coupled to an input of a first gray-to-binary converter, an input of a first gray-code generator, an input of a second set of flip flops, Art Unit: 2181

and an input of write clock logic that generates a first empty signal, a first full signal, and a first almost full signal:

an output of the first gray-to-binary converter coupled to the binary write pointer input:

an output of the first gray-code generator coupled to an input of the first set of flip flops;

an output of the second set of flip flops coupled to an input of read clock logic that generates a second empty signal, a second full signal, and a second almost full signal;

an output of a third set of flip flops coupled to an input of a second gray-to-binary converter, an input of a second gray-code generator, an input of a fourth set of flip flops, and the input of the read clock logic;

an output of the second gray-code generator coupled to an input of the third set of flip flops;

an output of the second gray-to-binary converter coupled to the binary read pointer input; and

an output of the fourth set of flip flops coupled to the input of the write clock logic.

#### Amend claim 31 to read:

The FIFO data system according to claim 27,

wherein the first set of flip flops, the fourth set of flip flops, the first gray-to-binary converter, the first gray-code generator and the write clock logic are part of a write clock Art Unit: 2181

domain, wherein the second set of flip flops, the third set of flip flops, the second grayto-binary converter, the second gray-code generator and the read clock logic are part of a read clock domain, and wherein the FIFO memory is part of both the write clock domain and the read clock domain

#### Amend claim 35 to read:

The FIFO data system according to claim 27, wherein the second set of flip flops transmit first gray codes.

## Amend claim 36 to read:

The FIFO data system according to claim 35, wherein the fourth set of flip flops transmit second gray codes.

## DETAILED ACTION

Claims 27 – 44 are pending.

## Continued Examination Under 37 CFR 1.114

3. A request for continued examination under 37 CFR 1.114 was filed in this application after a decision by the Board of Patent Appeals and Interferences, but before the filing of a Notice of Appeal to the Court of Appeals for the Federal Circuit or the commencement of a civil action. Since this application is eligible for continued examination under 37 CFR 1.114 and the fee set forth in 37 CFR 1.17(e) has been

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timely paid, the appeal has been withdrawn pursuant to 37 CFR 1.114 and prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 23 September 2009 has been entered.

## Allowable Subject Matter

- Claims 27 44 are allowed.
- 5. The following is an examiner's statement of reasons for allowance:

Claims 27 – 44 are allowed because the prior art of record fails to teach or suggest alone or in combination a first-in-first-out (FIFO) data system, comprising; a FIFO memory that has a binary write pointer input, a binary read pointer input, a write data input, a write clock input, and a read data output, wherein the FIFO memory comprises a data structure of depth d from which data may be written into using the write data input and read out using the read data output, wherein d is not a value of 2<sup>n</sup> in which d and n are integers; an output of a first set of flip flops coupled to an input of a first gray-to-binary converter, an input of a first gray-code generator, an input of a second set of flip flops, and an input of write clock logic that generates a first empty signal, a first full signal, and a first almost full signal; an output of the first gray-to-binary converter coupled to the binary write pointer input; an output of the first gray-code generator coupled to an input of the first set of flip flops; an output of the second set of flip flops coupled to an input of read clock logic that generates a second empty signal, a second full signal, and a second almost full signal; an output of a third set of flip flops

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coupled to an input of a second gray-to-binary converter, an input of a second gray-code generator, an input of a fourth set of flip flops, and the input of the read clock logic; an output of the second gray-code generator coupled to an input of the third set of flip flops; an output of the second gray-to-binary converter coupled to the binary read pointer input; and an output of the fourth set of flip flops coupled to the input of the write clock logic, as required by independent claim 27, in combination with other claimed limitations (emphasis added). Support for the above limitations can be found in the originally filed specification in Figure 1 and the accompanying detailed description of the figure. The prior art of record teaches using gray codes in addressing a FIFO memory which operates multiple different clock domains, but does not teach the circuit layout described in the claims to perform the addressing.

Claims 28 – 44 are allowed because of their dependency upon allowed independent claim 27.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to RICHARD FRANKLIN whose telephone number is (571)272-0669. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alford Kindred can be reached on (571) 272-4037. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Richard Franklin /RBF/ Patent Examiner Art Unit 2181

/NIKETA I. PATEL/ Primary Examiner, Art Unit 2181